

FIG. 1

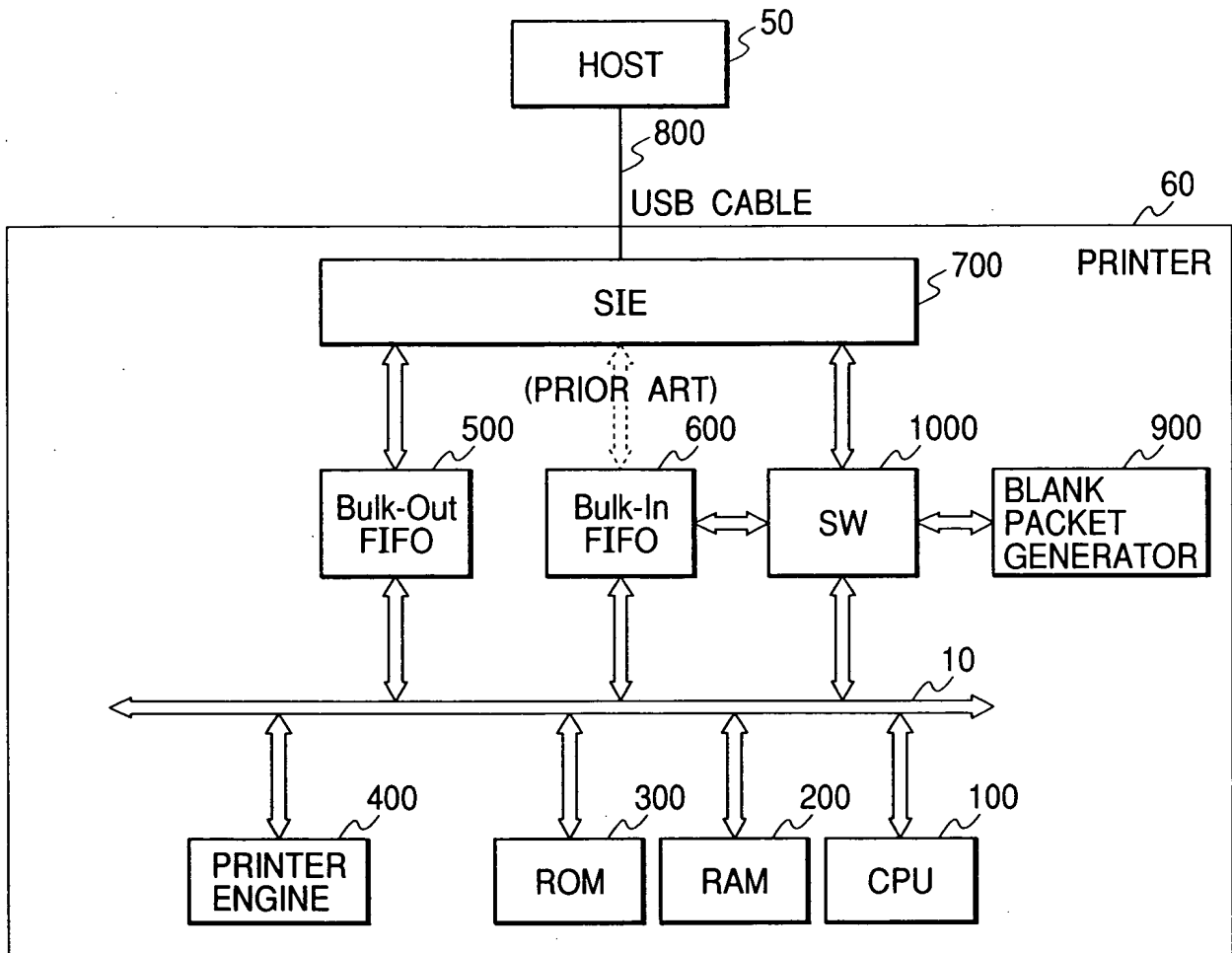


FIG. 2

EndPoint 0	Control
EndPoint 1	Bulk-Out
EndPoint 2	Bulk-In

FIG. 3

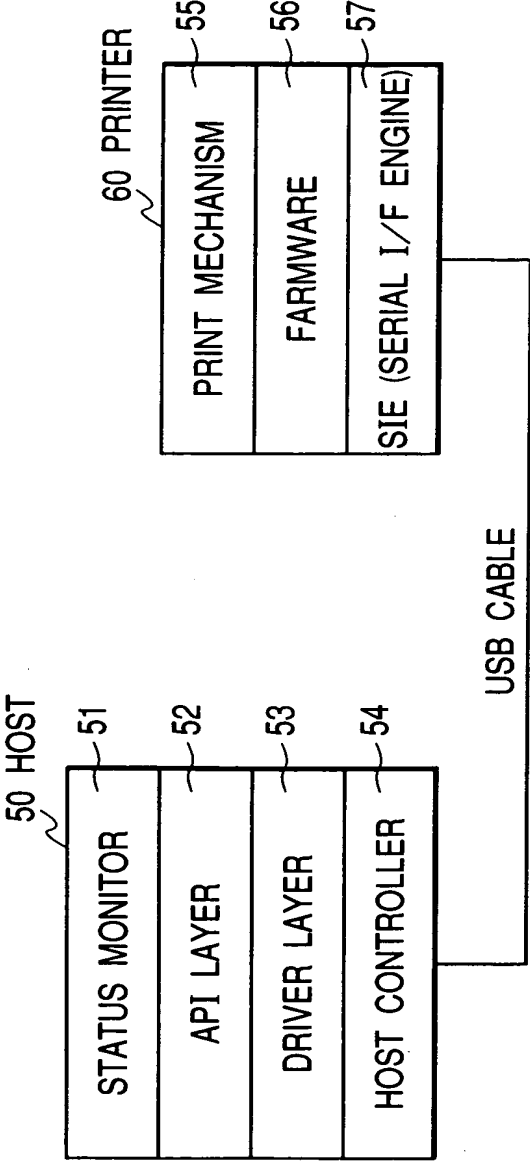


FIG. 4

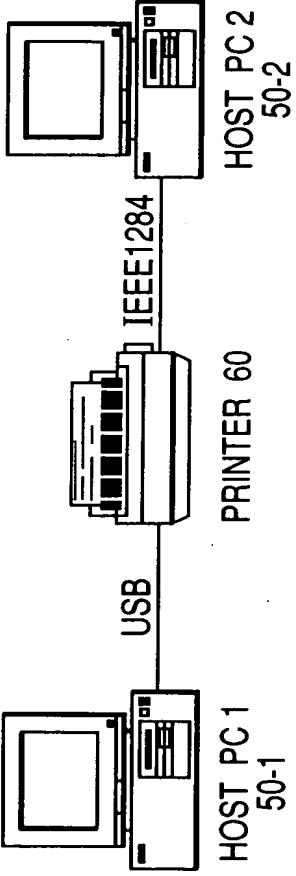


FIG. 5

Pin #	Pin assignments	Source
1	nStrobe	Host
2—9	Data	Bi-Di
10	nAck	Peripheral
11	Busy	Peripheral
12	PErr	Peripheral
13	Select	Peripheral
14	nAutoFeed	Host
15	nFault	Peripheral
16	nInit	Host
17	nSelectIn	Host
18—25	Signal Ground	

FIG. 6

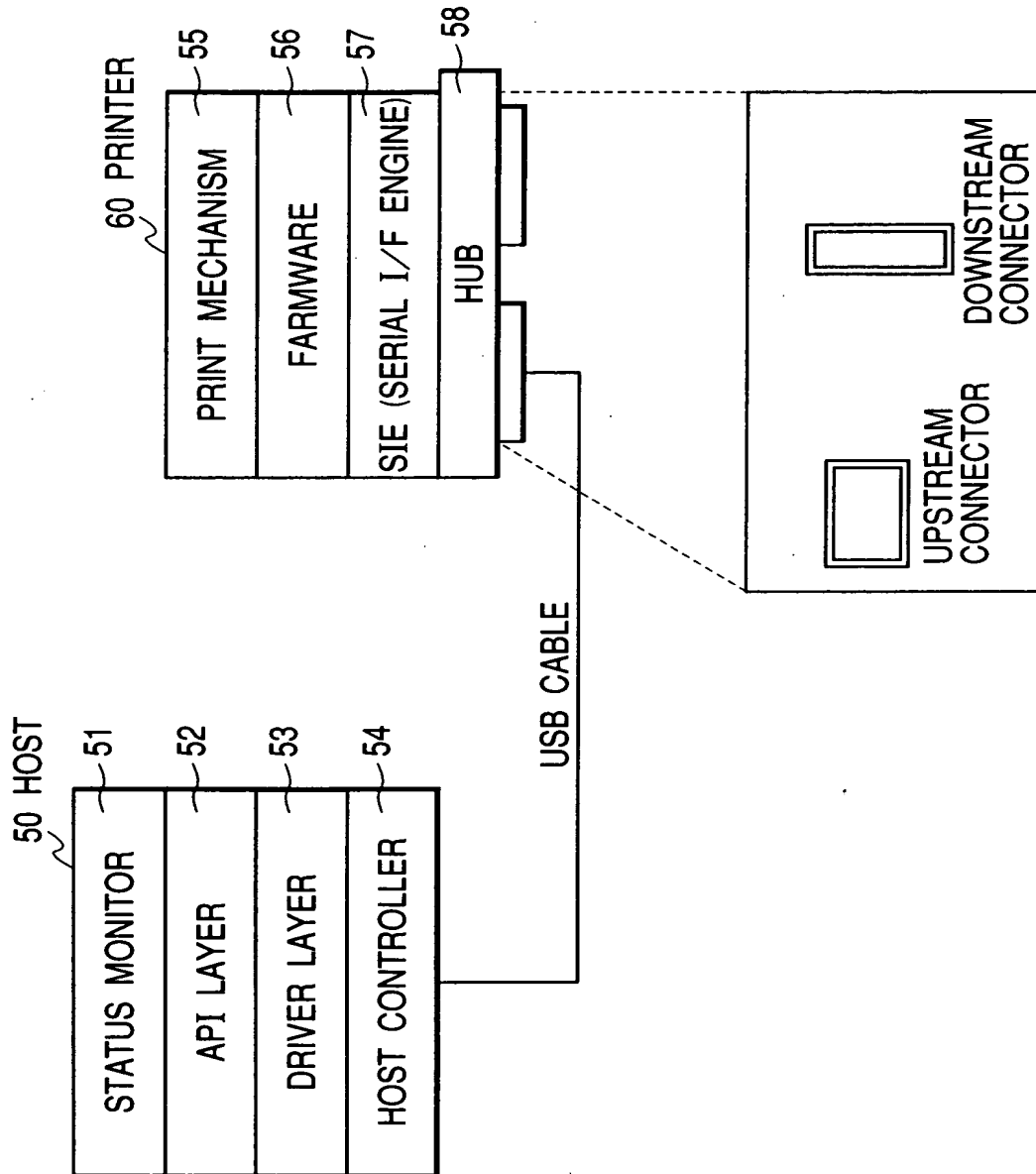


FIG. 7

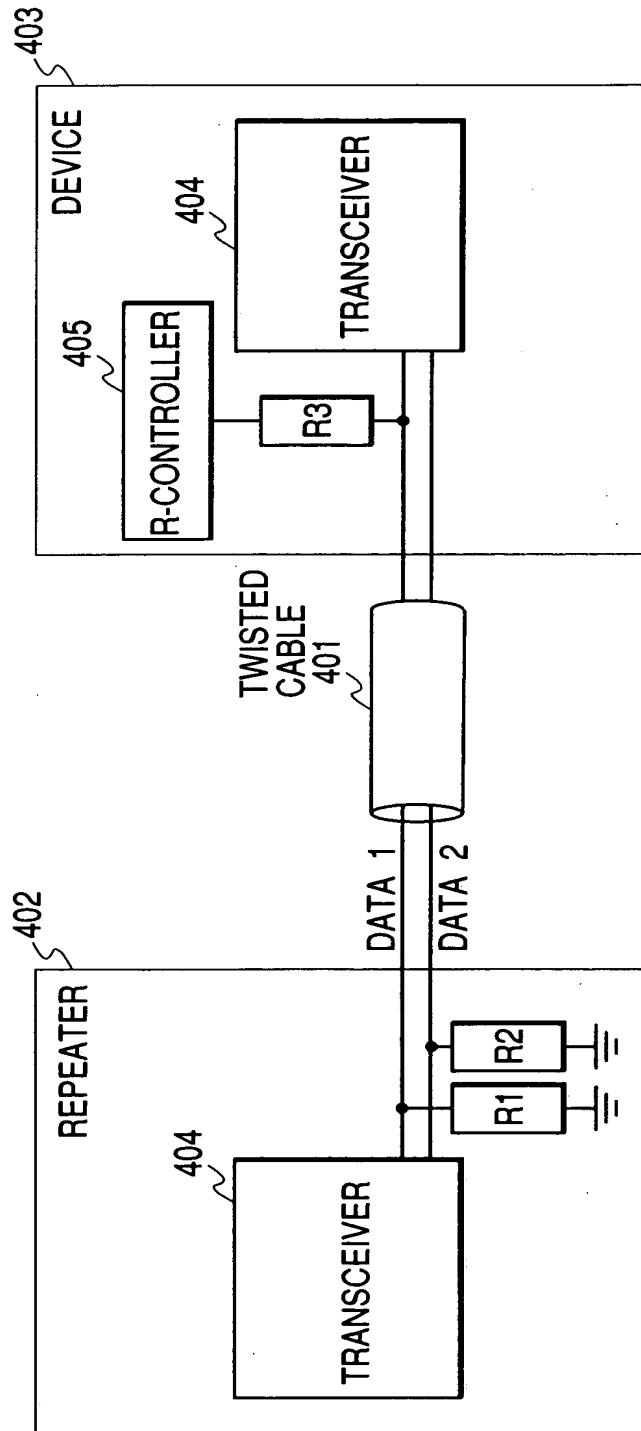


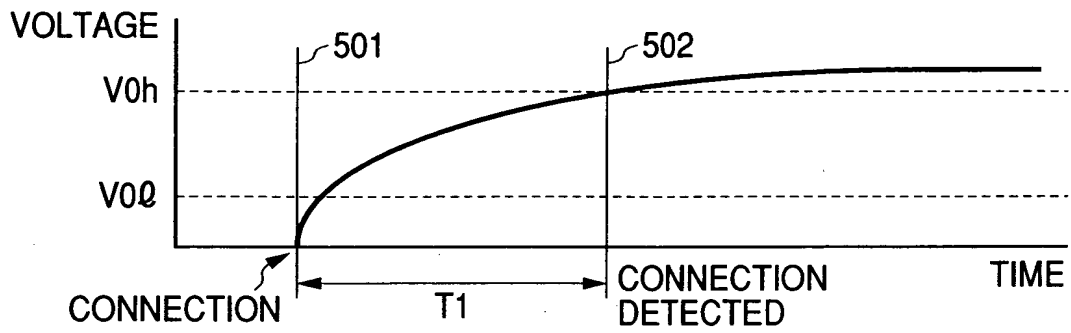
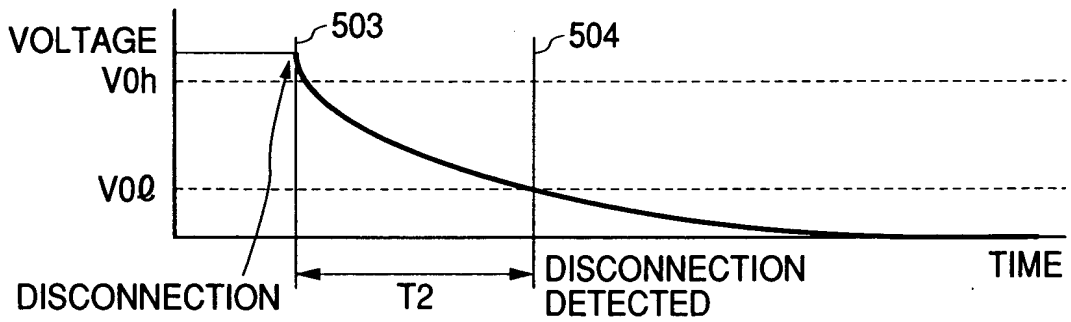
FIG. 8A*FIG. 8B*

FIG. 9

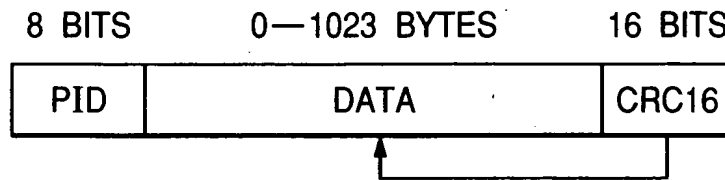


FIG. 10

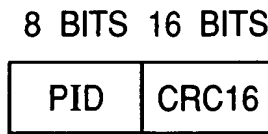


FIG. 11

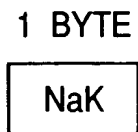


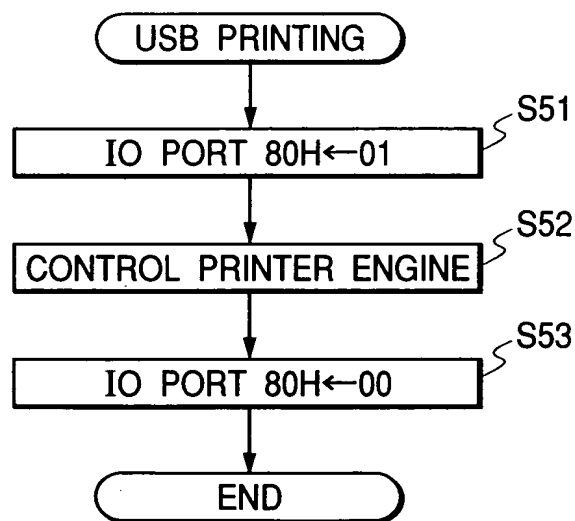
FIG. 12

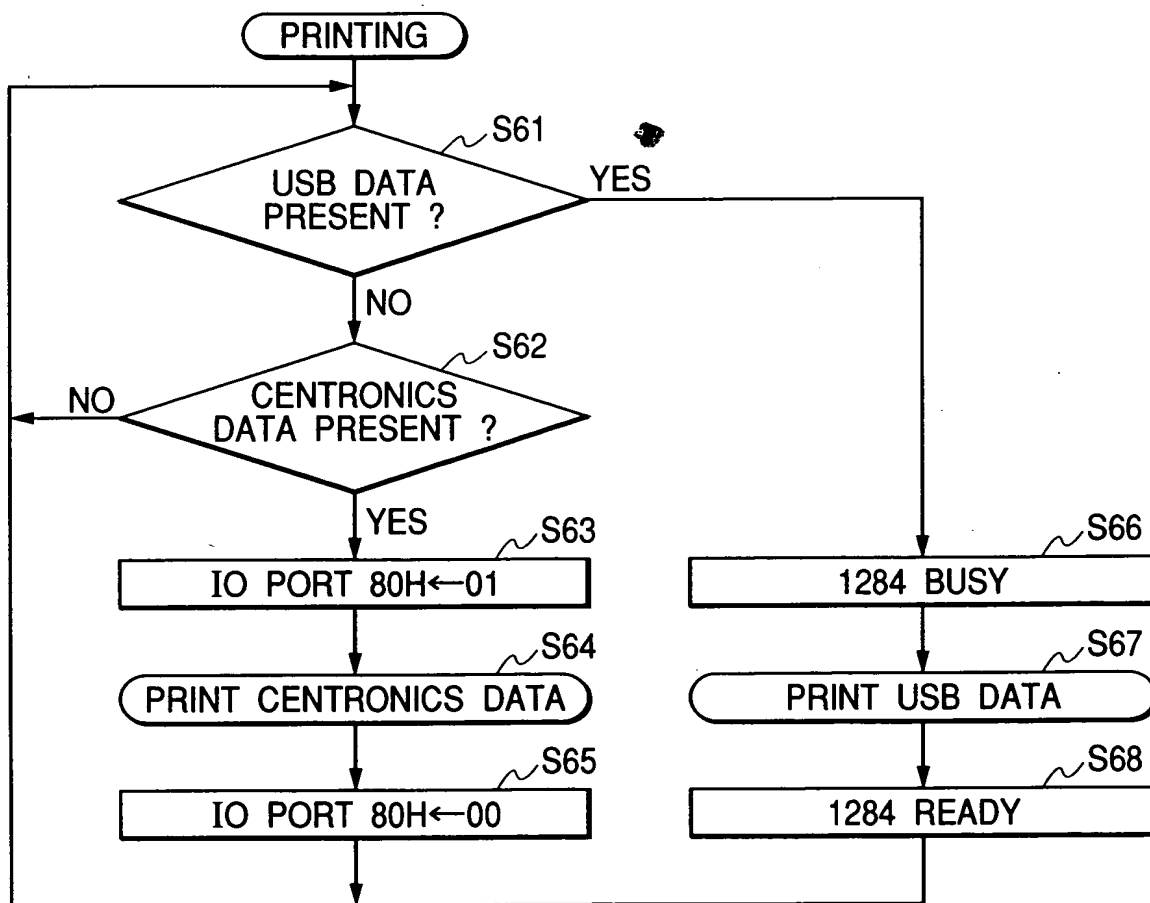
FIG. 13

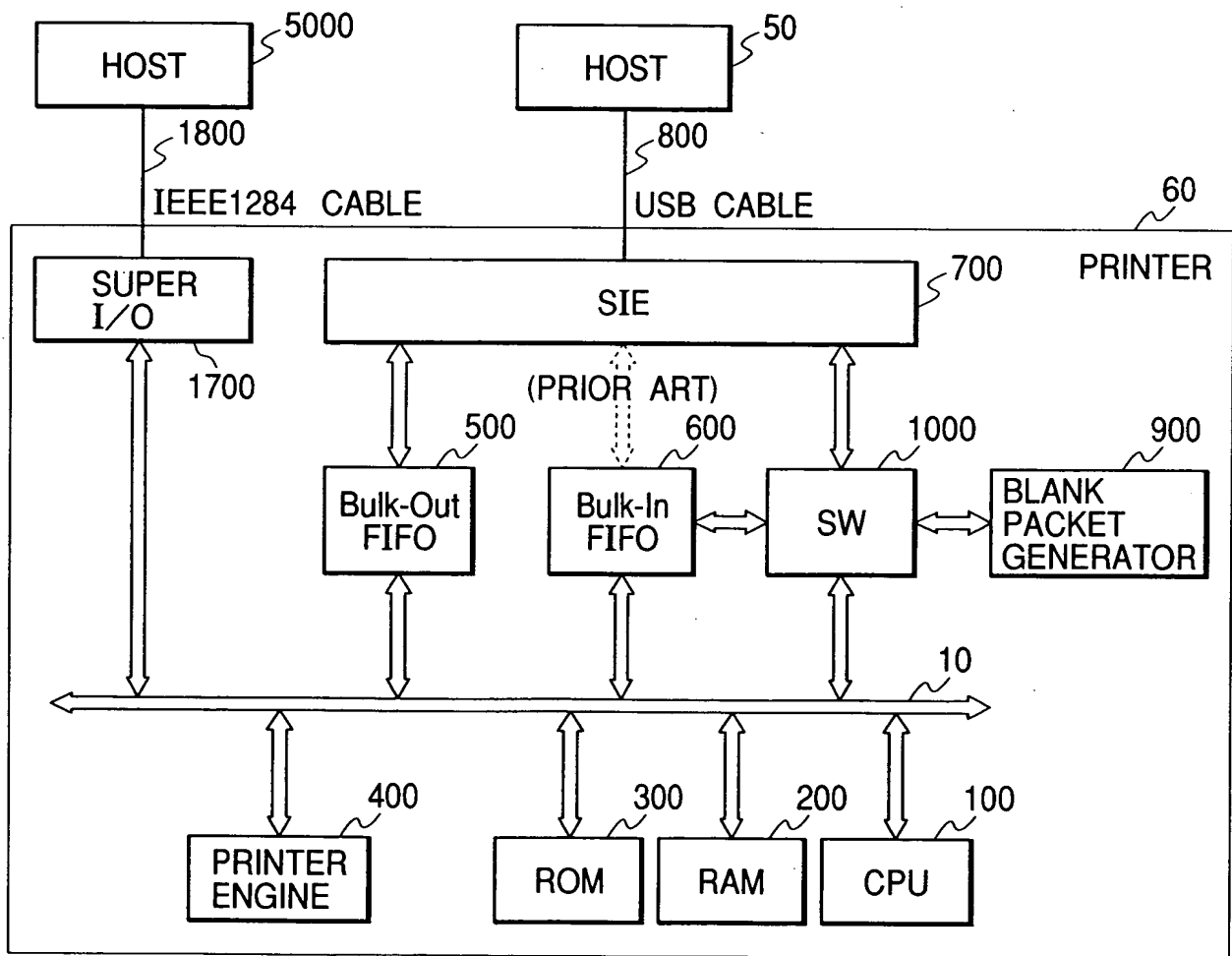
FIG. 14

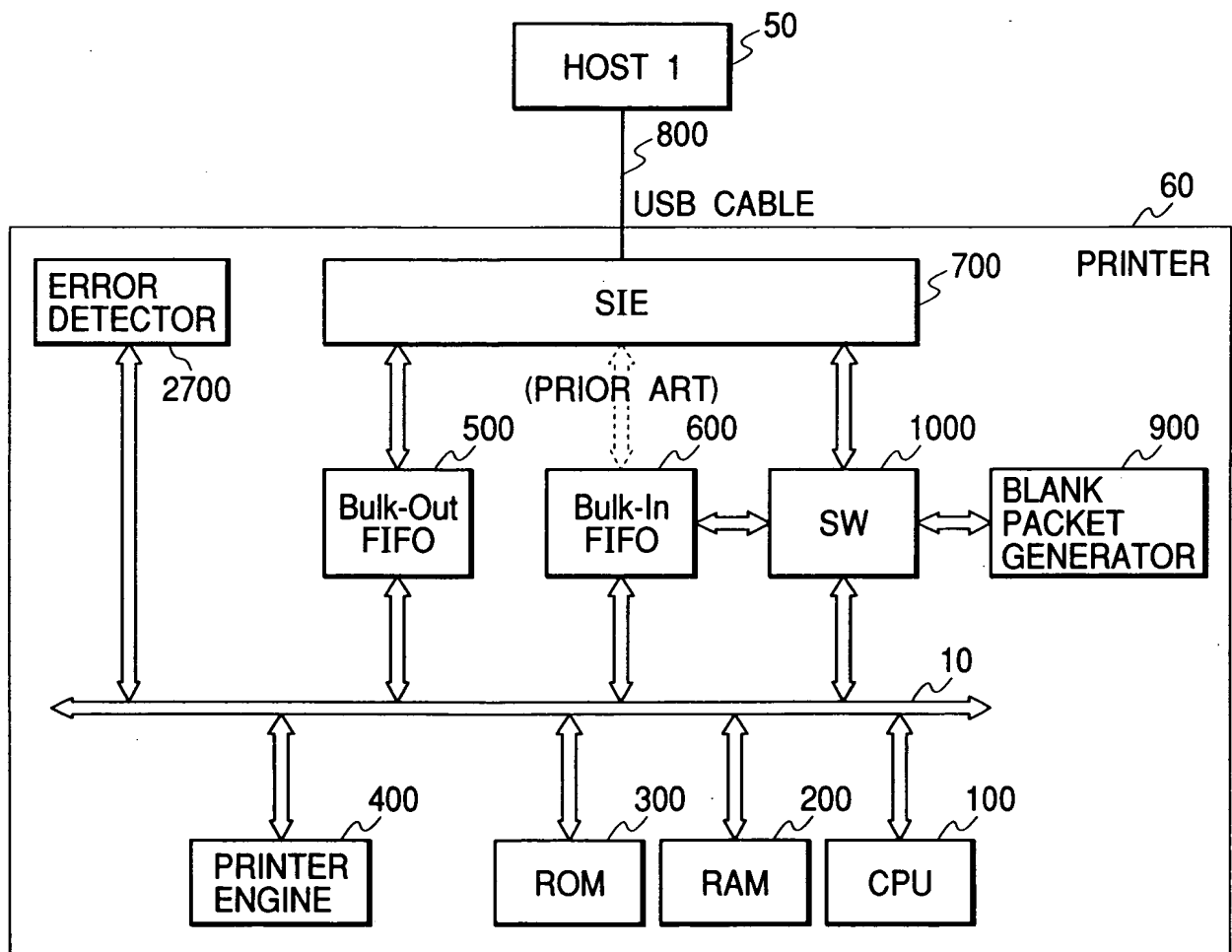
FIG. 15

FIG. 16